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PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	:	Examiner: Thanh T. Nguyen
TAKAO YONEHARA, ET AL.	)	
	:	Group Art Unit: 2813
Application No.: 10/059,171	)	
	:	
Filed: January 31, 2002	)	
	:	
For: METHOD OF MANUFACTURING	)	
A THIN-FILM SEMICONDUCTOR	:	
DEVICE USED FOR A DISPLAY	)	
REGION AND PERIPHERAL	:	
CIRCUIT REGION	)	September 15, 2004

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED NOTICE OF ALLOWANCE

Sir:

Applicants are in receipt of a Notice of Allowance dated September 7, 2004, in the above-referenced application. The Issue Fee is due on or before December 7, 2004, and has yet to be paid. In the interim, Applicants respectfully request issuance of a Corrected Notice of Allowance: because of a U.S. Patent and Trademark Office error, the title reads

“Method of Manufacturin a Thin-Film Semiconductor Device Used for a  
Display Region and Peripher Circuit Region”,

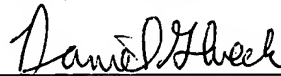
but should read, with emphasis added:

--Method of Manufacturing a Thin-Film Semiconductor Device Used for a  
Display Region and Peripheral Circuit Region--.

Favorable consideration and issuance of a Corrected Notice of Allowance is earnestly  
solicited.

Applicants' undersigned attorney may be reached in our Washington, D.C. office by  
telephone at (202) 530-1010. All correspondence should continue to be directed to our address  
given below.

Respectfully submitted,



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Attorney for Applicants

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